DEPARTMENT OF COMPUTER SCIENCE





| Course: B.Sc(Maj | jor, Minor) |) Year:I Semester:II | | | | |
|---------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----|------------|----------|-----------------------------------------------|
| Subjec | t | | PR | OBLEM SOLV | ING IN C | |
| Units | | General Fundamentals Introduction to C Decision Control and looping Statements Arrays and Functions Structure Union and Enumerated Data types Pointers | | | | |
| Durat | tion | | | 60hours | | |
| Learning Obj | ectives | To understand the evolution and functionality of a Digital Compute Apply logical skills to analyze a given problem. Develop an algorithm for solving a given problem. Understand 'C' language constructs like iterative statements, Arraprocessing, Pointers etc. Apply 'C' language constructs to the algorithm to write a 'C' language | | | | al Computer. ents, Array a 'C' language |
| Units | | III II2 II3 II4 | | | | |
| Hours Split:Total: 60 | | 10 12 14 10 14 | | | | |
| Internal valuation:40m | arks | 8 8 8 8 8 | | | | |
| Resource Material: | Study Mate 1. <u>https://ww</u> 2. <u>https://on</u> Reference F 1. E Balagur 2. Brain W Pearson pub 3. Ashok N Edition Pub 4. Yashavan YouTube L https://youtu Power Poin https://www QuestionBa https://www Bank-CPS.p | <pre> y Material(Handouts): ps://www.mcemotihari.ac.in/wpcontent/uploads/2019/11/file_5dc2a6c80c260.pd tps://onlinecourses.nptel.ac.in/noc20_cs06/preview rence Books: Balagurusamy – Programming in ANSIC – Tata McGraw-Hill publications. rain W Kernighan and Dennis M Ritchie - The 'C' Programming language" - son publications. shok N Kamthane: Programming with ANSI and Turbo C, Pearson ion Publications. ashavantKanetkar - Let Us 'C' – BPB Publications. Tube Links: y://youtu.be/8PopR3x-VMY er Point Presentations: y://www.slideshare.net/gauravjuneja11/c-language-ppt stionBank: y://www.acsce.edu.in/acsce/wp-content/uploads/2020/03/Module-wise-Question-k-CPS.pdf </pre> | | | | |

| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATION |
|------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|------------------------|
| I | Introduction to computer and programming: Introduction, Basic block diagram and functions of various components of computer, Concepts of Hardware and software, Types of software, Compiler and interpreter, Concepts of Machine level, Assembly level and high-level programming, Flowcharts and Algorithms Fundamentals of C: History of C, Features of C, C Tokens-variables and keywords and identifiers, constants and Data types, Rules for constructing variable names, Operators, Structure of C program, Input /output statements in C-Formatted and Unformatted I/O | P1,P2,P3 | PQ,P6,PT |
| п | Control statements: Decision making statements: if, if else, else if ladder, switch statements. Loop control statements: while loop, for loop and do-while loop. Jump Control statements: break, continue and go to. | P1,P2,P3,P5 | P6,PT |
| III | Derived data types in C: Arrays: One Dimensional arrays - Declaration, Initialization and Memory representation; Two Dimensional arrays -Declaration, Initialization and Memory representation. Strings: Declaring & Initializing string variables; String handling functions, Character handling functions. | P1,P2,P3,P5 | PQ,PT |
| IV | Functions: Function Prototype, definition and calling. Return statement. Nesting of functions. Categories of functions. Recursion, Parameter Passing by address & by value. Local and Global variables. Storage classes: automatic, external, static and register. Pointers: Pointer data type, Pointer declaration, initialization, accessing values using pointers. Pointer arithmetic. Pointers and arrays, pointers and functions. | P1,P2,P4 | PQ,P6,PT |

| v | Dynamic Memory Management: Introduction, Functions- malloc, calloc, realloc, free Structures: Basics of structure, structure members, accessing structure members, nested structures, array of structures, structure and functions, structures and pointers. Unions - Union definition; differencebetween Structures and Unions | PQ,P6,PT, P8 | PQ,PT |
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| Course: B.Sc | | Year:I | | Semester | :II | |
|-------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------|---|----------|-----|--|
| Subject | DIGITAL LOGIC DESIGN | | | | | |
| Units | Number Systems Logic Gates and Boolean Algebra Combinational Logic Circuits – 1 Combinational Logic Circuits – 2 Sequential Logic Circuits | | | | | |
| Duration | 60hours | | | | | |
| Learning Objectives | Understand how to Convert numbers from one radix to another radix and perform arithmeticoperations. Simplify Boolean functions using Boolean algebra and k- maps Design adders and sub tractors circuits Design combinational logic circuits such as decoders, encoders, multiplexers Use flip flops to design registers and counters. | | | | | |
| Units | U1 U2 U3 U4 U5 | | | | | |
| Hours Split:Total:60 | 10 12 14 10 14 | | | | | |
| Internal valuation:40marks | 8 | 8 | 8 | 8 | 8 | |

| Resource Material: | StudyMaterial(Handouts): https://youtu.be/1prCy3kdy1U https://youtu.be/7bVnsXHO6Uw |
|-----------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | Reference Books: |
| | Kohavi, Jha, "Switching and Finite Automata Theory", 3rd edition, Cambridge. Leach, Malvino, Saha, "Digital Principles and Applications", 7th edition, TMH. Roth, "Fundamentals of Logic Design", 5th edition, Cengage |
| | YouTube Links: https://youtu.be/INEtYZqtjTo https://youtu.be/Q9gmAgwT6o8 |
| | Power Point Presentations: https://youtu.be/EtkD1-0WR7I https://youtu.be/0lwhoQ5aQe8 |

| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATION |
|------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|------------------------|
| I | Number Systems: Binary, octal, decimal, hexadecimal number systems, conversion of numbers from one radix to another radix, r's, (r-1)'s complements, signed binary numbers, addition and subtraction of unsigned and signed numbers, weighted and unweighted code | P1,P3,P2,P6 | PQ,PT |
| Π | Logic Gates and Boolean Algebra: NOT, AND, OR, universal gates, X-OR and X-NOR gates, Boolean laws and theorems, complement and dual of a logic function, canonical and standard forms, two level realization of logic functions using universal gates, minimizations of logic functions (POS and SOP) using Boolean theorems, K-map (up to four variables), don't care conditions | P1,P5,P3,P2 | P6,PT,PQ |
| III | Combinational Logic Circuits – 1: Design of half adder, full adder, half subtractor, fullsubtractor, ripple adders and subtractors, ripple adder / subtractor. | P1,P3,P5,P7 | P6,PT |
| IV | Combinational Logic Circuits – 2: Design of decoders, encoders, priority encoder, multiplexers, demultiplexers, higher order decoders, demultiplexers and multiplexers, realization of Boolean functions using decoders, multiplexers | P1,P2,P5,P6 | PQ, PT |
| V | Sequential Logic Circuits: Classification of sequential circuits, latch and flip-flop, RS- latch using NAND and NOR Gates, truth tables, RS, JK, T and D flip-flops, truth and excitation tables, conversion of flip- flops, flip-flops with asynchronous inputs (preset and clear). Design of registers, shift registers, bidirectional shift registers, universal shift register, design of ripple counters, synchronous counters and variable modulus counters | PQ,P7,PT, P8 | PQ,P6,PT |

| Course: B.Sc | Year:II Semester:III | | | | | |
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| Subject | Object Oriented Programming using Java | | | | | |
| Units | 1.OOPs Concepts and Java Programming 2.Classes & Objects, Inheritance, Polymorphism 3.Interface, Packages, Exception Handling 4.Multithreading, Stream based I/O (java.io) 5. GUI Programming with Swing, Event Handling | | | | | |
| Duration | 60hours | | | | | |
| Learning Objectives | 60hours 1. Understand the basic concepts of Object-Oriented Programming and Java Program Constructs 2. Implement classes and objects and analyze Inheritance and Dynamic Method Dispatch 3. Demonstrate various classes in different packages and can design own packages 4. Manage Exceptions and Apply Threads 5. Create GUI screens along with event handling | | | | | |
| Units | U1 | U2 | | U3 | U4 | U5 |
| Hours Split: Total: 60 | 10 12 14 10 14 | | | | | |
| Internal valuation:40marks | 8 | 8 | | 8 | 8 | 8 |

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| rson, 2015. v. Press. |
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| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATI ON |
|------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|----------------------------|
| Ι | OOPs Concepts and Java Programming: Introduction to Object-Oriented concepts, procedural and object-oriented programming paradigm Java programming: An Overview of Java, Java Environment, Data types, Variables, constants, scope and life time of variables, operators, type conversion and casting, Accepting Input from the Keyboard, Reading Input with Java.util.Scanner Class, Displaying Output with System.out.printf(), Displaying Formatted Output with String.format(), Control Statements | P1,P2,P3 | PQ,P6,PT |
| II | Arrays, Command Line Arguments, Strings-String Class Methods Classes & Objects: Creating Classes, declaring objects, Methods, parameter passing, static fields and methods, Constructors, and 'this' keyword, overloading methods and access Inheritance: Inheritance hierarchies, super and subclasses, member access rules, 'super' keyword, preventing inheritance: final classes and methods, the object class and its methods; Polymorphism: Dynamic binding, method overriding, abstract classes and methods; | P1,P2,P3,P5 | P6,PT |
| III | Interface: Interfaces VS Abstract classes, defining an interface, implement interfaces, accessing implementations through interface references, extending interface; Packages: Defining, creating and accessing a package, understanding CLASSPATH, importing packages. Exception Handling: Benefits of exception handling, the classification of exceptions, exception hierarchy, checked exceptions and unchecked exceptions, usage of try, catch, throw, throws and finally, rethrowing exceptions, exception sub classes. | P1,P2,P3,P5 | PQ,PT |
| IV | Multithreading: Differences between multiple processes and multiple threads, thread states, thread life cycle, creating threads, interrupting threads, thread priorities, synchronizing threads, inter thread communication. Stream based I/O (java.io) – The Stream classes-Byte streams and Character streams, Reading console Input and Writing Console Output, File class, Reading and writing Files, The Console class, Serialization | P1,P2,P4 | PQ,P6,PT |
| V | GUI Programming with Swing- Introduction, MVC architecture, components, containers. Understanding Layout Managers - Flow Layout, Border Layout, Grid Layout, Card Layout, Grid Bag Layout. Event Handling- The Delegation event model- Events, Event sources, Event Listeners, Event classes, Handling mouse and keyboard events, Adapter classes, Inner classes, Anonymous Inner classes | PQ,P6,PT, P8 | PQ,PT |

| Course: B.Sc | Year: | п | S | emester:III | |
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| Subject | | Dat | ta Structures usin | g C | |
| Units | Data Structures using C Linked List, Implementation of Linked List ADT Stacks, Queues Searching, Sorting Binary Trees, Graphs | | | | |
| Duration | | | 60hours | | |
| Learning Objectives | Understand various Data Structures for data storage and processing. Realize Linked List Data Structure for various operations Analyze step by step and develop algorithms to solve real world problems by implementing Stacks, Queues data structures. Understand and implement various searching & sorting techniques. Understand the Non-Linear Data Structures such as Binary Trees and Graphs | | | | |
| Units | U1 | U2 | U3 | U4 | U5 |
| Hours Split: Total: 60 | 10 | 12 | 14 | 10 | 14 |
| Internal valuation:40marks | 8 | 8 | 8 | 8 | 8 |

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| | Study Material(Handouts): https://youtu.be/ASAo20o_EXs |
| Resource | |
| Matarial | Deference Reeks |
| | 1 "Dete Structures through C" Verhausst Versther DDD |
| | 1. "Data Structures inrough C", Yashavani Kanetkar, BPB |
| | Publications |
| | 2. Rajesh K. Shukla, "Data Structure Using C |
| | and C++" Wiley Dreamtech Publication. |
| | 3. Lipschutz, "Data Structures" Schaum's Outline Series, Tata |
| | Mcgraw-hill Education (India) Pvt. Ltd. |
| | 4. Michael T. Goodrich, Roberto Tamassia, David M. Mount |
| | "Data Structures and Algorithms in C++", Wiley India. |
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| | VonTube Links |
| | https://woutu.bo/B31L gI/V/DO |
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| | https://youtu.be/MtVZAXepMPM |
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| | Power Point Presentations |
| | |
| | https://voutu.be/vAZvJUViLLU |
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| | https://youtu.be/7WCaO_pgFs8 |
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| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATION |
|------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|------------------------|
| I | Basic Concepts: Pointers and dynamic memory allocation, Algorithm-Definition and characteristics, Algorithm Analysis-Space Complexity, Time Complexity, Asymptotic Notation Introduction to Data structures: Definition, Types of Data structure, Abstract Data Types (ADT), Difference between Abstract Data Types, Data Types, and Data Structures. Arrays-Concept of Arrays, Single dimensional array, Two dimensional array, Operations on arrays with Algorithms (searching, traversing, inserting, deleting) | P1,P2,P3 | PQ,P6,PT |
| п | Linked List: Concept of Linked Lists, Representation of linked lists in Memory, Comparison between Linked List and Array, Types of Linked Lists - Singly Linked list, Doubly Linked list, Circularly Singly Linked list, Circularly Doubly Linked list; Implementation of Linked List ADT: Creating a List, Traversing a linked list, Searching linked list, Insertion and deletion into linked list (At first Node, Specified Position, Last node), Application of linked lists | P1,P2,P3,P5 | P6,PT |
| ш | Stacks: Introduction to stack ADT, Representation of stacks with array and Linked List, Implementation of stacks, Application of stacks - Polish Notations - Converting Infix to Post Fix Notation - Evaluation of Post Fix Notation - Tower of Hanoi, Recursion: Concept and Comparison between recursion and Iteration Queues: Introduction to Queue ADT, Representation of Queues with array and Linked List, Implementation of Queues, Application of Queues Types of Queues-Circular Queues, Priority Queue | P1,P2,P3,P5 | PQ,PT |
| IV | Searching: Linear or Sequential Search, Binary Search and Indexed Sequential Search Sorting: Selection Sort, Bubble Sort, Insertion Sort, Quick Sort and Merge Sort | P1,P2,P4 | PQ,P6,PT |
| V | Binary Trees: Concept of Non- Linear Data Structures, Introduction Binary Trees, Types of Trees, Basic Definition of Binary Trees, Properties of Binary Trees, Representation of Binary Trees, Operations on a Binary Search Tree, Binary Tree Traversal, Applications of Binary Tree. Graphs: Introduction to Graphs, Terms Associated with Graphs, Sequential Representation of Graphs, Linked Representation of Graphs, Traversal of Graphs (DFS, BFS), Application of Graphs. | PQ,P6,PT, P8 | PQ,PT |

| Course: B.Sc | | Year:II Semester:III | | | | |
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| Subje | ect | | C | omputer Orga | nization | |
| Unit | s | Register Transfer Language and Micro Operations. CPU and Micro Programmed Control. Memory Organization Input-Output Organization Computer Arithmetic and Parallel Processing. | | | | |
| Dura | ation | | | 60ha | ours | |
| Learning Objectives | | Identify different types of instructions Differentiate between micro-programmed and hard-wired control units. Analyse the performance of hierarchical organization of memory. Summarize different data transfer techniques. Demonstrate arithmetic operations on fixed- and floating-point numbers and illustrate concepts of parallel processing | | | | |
| Units | | U1 | U2 | U3 | U4 | U5 |
| Hours Split: Total: 60 | | 10 | 12 | 14 | 10 | 14 |
| Internal valuation:40marks | | 8 | 8 | 8 | 8 | 8 |
| Resource Material: | Study Mater https://youtu https://youtu Reference Ba 1. Carl "Cor 2. Willi Arch YouTube Lin https://youtu https://youtu https://youtu | y Material(Handouts): :://youtu.be/CDO28Esqmcg :://youtu.be/O18D69VKX2k rence Books: . Carl Hamacher, ZvonksVranesic, SafeaZaky, "Computer Organization", 5th edition, McGraw Hill. . William Stallings, "Computer Organization and Architecture", 8th edition, Pearson/PHI Fube Links: :://youtu.be/oAneKttKjtA :://youtu.be/oAneKttKjtA :://youtu.be/ucUHpSYm08U er Point Presentations: :://youtu.be/A3t7ckliyaA :://youtu.be/OZjb6vy0vy4 | | | | |

| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATION |
|------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|------------------------|
| I | Register Transfer Language and Micro Operations: Introduction- Functional units, computer registers, register transfer language, register transfer, bus and memory transfers, arithmetic, logic and shift micro-operations, arithmetic logic shift unit. Basic Computer Organization and Design: Instruction codes, instruction cycle. Register reference instructions, Memory – reference instructions, input – output and interrupt | P1,P2,P3 | PQ,P6,PT |
| п | CPU and Micro Programmed Control: Central Processing unit: Introduction, instruction formats, addressing modes.Control memory, address sequencing, design of control unit - hard wired control, micro programmed control | P1,P2,P3,P5 | P6,PT |
| III | Memory Organization: Memory hierarchy, main memory, auxiliary memory, associative memory, cache Memory and mappings. | P1,P2,P3,P5 | PQ,PT |
| IV | Input-Output Organization: Peripheral Devices, input-output interface, asynchronous data transfer, modes of transfer- programmed I/O, priority interrupt, direct memory access, Input – Output Processor (IOP). | P1,P2,P4 | PQ,P6,PT |
| V | Computer Arithmetic and Parallel Processing: Data representation- fixed point, floating point, addition and subtraction, multiplication and division algorithms. Parallel Processing-Parallel Processing, Pipelining, Arithmetic Pipeline, Instruction Pipeline. | PQ,P6,PT, P8 | PQ,PT |

| Course:] | B.Sc | Year:II Semester:III | | | | | |
|---------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------|------|---------------|--------------------------|----|
| Subje | ect | | (| Oper | ating Systems | | |
| Units | 5 | 1.Introduction to operating systems 2. Process Scheduling and threads 3.Process Management and deadlocks 4.Memory management 5. File and I/O Management | | | | | |
| Durs | otion | | unugennent, | | 60hours | | |
| Duration Learning Objectives | | Demonstrate knowledge and comprehension of operating system functions. Analyze different process scheduling algorithms and apply them to manage processes and threads effectively Create strategies to prevent, detect, and recover from deadlocks, and design solutions for inter-process communication and synchronization problems. Compare and contrast different memory allocation strategies and evaluate their effectiveness Evaluate disk scheduling algorithms while implementing OS security measures | | | | | |
| Units | | U1 | U2 | | U3 | U4 | U5 |
| Hours Split: Total: 60 | | 10 | 12 | | 14 | 10 | 14 |
| Internal valuation:40marks | | 8 | 8 | | 8 | 8 | 8 |
| Resource Material: | Study Mater https://youtu https://youtu. Reference Ba 1. Operating 2. Operating 3. Operating Gagne (7th I YouTube Lin https://youtu https://youtu https://youtu https://youtu | 8 8 8 8 'Material(Handouts): //youtu.be/WtNoaKUcawY /youtu.be/al14MceYHaQ ence Books: erating Systems: Internals and Design Principles by Stallings (Pearson) erating Systems by J. Archer Harris (Author), Jyoti Singh (Author) (TMH) erating System Principles by Abraham Silberschatz, Peter Baer Galvin and Greg e (7th Edition) Wiley India Edition ube Links: //youtu.be/ACsLvXuaKxw //youtu.be/MYgmmJJfdBg r Point Presentations: //youtu.be/W3BSLvavPAs //youtu.be/xsbT36yVWKg | | | | n) FMH) n and Greg | |

| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATI ON |
|------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|----------------------------|
| I | What is Operating System? History and Evolution of OS, Basic OS functions, Resource Abstraction, Types of Operating Systems– Multiprogramming Systems, Batch Systems, Time Sharing Systems; Operating Systems for Personal Computers, Workstations and Hand-held Devices, Process Control & Real time Systems. | P1,P2,P3 | PQ,P6,PT |
| п | Processor and User Modes, Kernels, System Calls and System Programs, System View of the Process and Resources, Process Abstraction, Process Hierarchy, Threads, Threading Issues, Thread Libraries; Process Scheduling- Non-Preemptive and Preemptive Scheduling Algorithms. | P1,P2,P3,P5 | P6,PT |
| III | Process Management: Deadlock, Deadlock Characterization, Necessary and Sufficient Conditions for Deadlock, Deadlock Handling Approaches: Deadlock Prevention, Deadlock Avoidance and Deadlock Detection and Recovery. Concurrent and Dependent Processes, Critical Section, Semaphores, Methods for Inter process Communication; Process Synchronization, Classical Process Synchronization Problems: Producer-Consumer, Reader-Writer. | P1,P2,P3,P5 | PQ,PT |
| IV | Memory Management: Physical and Virtual Address Space; Memory Allocation Strategies–Fixed and - Variable Partitions, Paging, Segmentation, Virtual Memory. | P1,P2,P4 | PQ,P6,PT |
| V | File and I/O Management, OS security: Directory Structure, File Operations, File Allocation Methods, Device Management, Pipes, Buffer, Shared Memory, Disk Scheduling algorithms. | PQ,P6,PT ,P8 | PQ,PT |

| Course: B | B.Sc | Year:II Semester:IV | | | | | |
|---------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----|--------|-------------|-------------|----|
| Subjec | et | |] | Databa | ase Managem | ent Systems | |
| Units | | 1.Overview of Database Management System 2.Entity-Relationship Model 3.Relational Model 4.Structured Query Language 5.PL/SQL | | | | | |
| Durat | tion | | | | 60hours | | |
| Learning Obj | jectives | Differentiate between database systems and file based systems Design a database using ER model Use relational model in database design Use SOL commenda for creating and maximulating data story data | | | | | |
| | | databases. | | | | | |
| Units | | U1 | U2 | | U3 | U4 | U5 |
| Hours Split: Total: 60 | | 10 | 12 | | 14 | 10 | 14 |
| Internal valuation:40m | narks | 8 | 8 | | 8 | 8 | 8 |
| Resource Material: | Study Mate https://yout https://yout Reference B 1. Database 2. Principles 3. Fundamo 4. SQL: The YouTube Li https://yout https://yout https://yout | 8 8 8 8 atterial(Handouts): autu.be/dl00fOOYLOM autu.be/dl00fOOYLOM autu.be/49Urokq3k7A e Books: use Management Systems by Raghu Ramakrishnan, McGrawhill oles of Database Systems by J. D. Ullman mentals of Database Systems by R. Elmasri and S. Navathe Che Ultimate Beginners Guide by Steve Tale. Links: autu.be/c5HAwKX-suM autu.be/c5HAwKX-suM autu.be/mh26fgrO-9k sint Presentations: outu.be/T2zolcNJRw8 | | | | | |

| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATI ON |
|------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|----------------------------|
| Ι | Introduction to data, information, database, database management systems, file-based system, Drawbacks of file-Based System, database approach, Classification of Database Management Systems, advantages of database approach, Various Data Models, Components of Database Management System, three schema architecture of data base, costs and risks of database approach. | P1,P2,P3 | PQ,P6,PT |
| П | Introduction, the building blocks of an entity relationship diagram, classification of entity sets, attribute classification, relationship degree, relationship classification, reducing ER diagram to tables, enhanced entity-relationship model (EER model), generalization and specialization, IS A relationship and attribute inheritance, multiple inheritance, constraints on specialization and generalization, advantages of ER modeling. | P1,P2,P3,P5 | P6,PT |
| III | Introduction, CODD Rules, relational data model, concept of key, relational integrity, relational algebra, relational algebra operations, advantages of relational algebra, limitations of relational algebra, relational calculus, tuple relational calculus, domain relational Calculus (DRC), Functional dependencies and normal forms upto 3rd normal form. | P1,P2,P3,P5 | PQ,PT |
| IV | Introduction, Commands in SQL, Data Types in SQL, Data Definition Language, Selection Operation, Projection Operation, Aggregate functions, Data Manipulation Language, Table Modification Commands, Join Operation, Set Operations, View, Sub Query. | P1,P2,P4 | PQ,P6,PT |
| V | Introduction, Shortcomings of SQL, Structure of PL/SQL, PL/SQL Language Elements, Data Types, Operators Precedence, Control Structure, Steps to Create a PL/SQL, Program, Iterative Control, Procedure, Function, Database Triggers, Types of Triggers | PQ,P6,PT ,P8 | PQ,PT |

| Course: B.Sc | Year: | II | | S | emester:IV | |
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| Subject | Object Or | iented Softv | ware E | Engineering | | |
| Units | Introduction to Object-Oriented Programming Requirements Analysis and Design Software Construction and Testing Software Maintenance and Evolution Advanced Topics in Object-Oriented Software Engineering | | | | | |
| Duration | | | | 60hours | | |
| Learning Objectives | Understand and apply the fundamental principles of Object-Oriented Programming (OOP) concepts and Unified Modeling Language (UML) basics, in the development of software solutions. Analyze and specify software requirements, develop use cases and scenarios, apply object- oriented analysis and design (OOAD) principles Familiar with the concept of test-driven development (TDD) and its practical implementation Analyze and Evaluate Software Maintenance and Evolution Strategies Apply Advanced Object-Oriented Software Engineering Concepts | | | | | |
| Units | U1 | U2 | | U3 | U4 | U5 |
| Hours Split: Total: 60 | 10 | 12 | | 14 | 10 | 14 |
| Internal valuation:40marks | 8 | 8 | | 8 | 8 | 8 |

| | Study Material(Handouts): |
|----------|--------------------------------------------------------------------------------------|
| | https://youtu.be/1UVA4Ixbwwc |
| Resource | https://youtu.be/BaVaila7_vI |
| Motoriol | |
| Materia: | |
| | Reference Books: |
| | 1. Requirements engineering: processes and techniques, G.Kotonya and, I.Sommerville, |
| | 1998 , Wiley |
| | 2. Design Patterns, E.Gamma, R. Helm, R. Johnson, and J. Vlissides |
| | 3. The Unified Modeling Language Reference Manual, J. Rumbaugh, L.Jacobson and G. |
| | Booch Addison Wesley |
| | Dooch, Addison Wesley |
| | |
| | YouTube Links: |
| | https://youtu.be/efW8lH6A21Q |
| | https://youtu.be/24fJ_ZKo8LA |
| | Power Point Presentations: |
| | https://youtu.be/-b1rd |
| | |
| | https://woutu.he/VnVHgi6OPrO |
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| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATI ON |
|------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|----------------------------|
| I | Overview of software engineering, Introduction to Object-Oriented Programming (OOP) concepts (classes, objects, inheritance, polymorphism), Unified Modelling Language (UML) basics, Introduction to software development process and software development life cycle (SDLC). | P1,P2,P3 | PQ,P6,PT |
| П | Requirements analysis and specification, Use cases and scenarios, Object-oriented analysis and design (OOAD), Design patterns, UML modelling techniques (class diagrams, sequence diagrams, state machine diagrams, activity diagrams) | P1,P2,P3,P5 | P6,PT |
| III | Software construction basics, Object-oriented design principles, Object-oriented programming languages (Java, C++, Python), Software testing basics (unit testing, integration testing, system testing), Test-driven development (TDD) | P1,P2,P3,P5 | PQ,PT |
| IV | Software maintenance basics, refactoring techniques Software version control, Code review and inspection, Software evolution and reengineering | P1,P2,P4 | PQ,P6,PT |
| V | Model-driven engineering (MDE), Aspect-oriented programming (AOP), Component-based software engineering (CBSE), Service- oriented architecture (SOA), Agile software development and Scrum methodologies. | PQ,P6,PT, P8 | PQ,PT |

| Course: B.Sc | Year: | I | S | emester:IV | |
|-------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------|------------------|------------|----|
| Subject | Data Con | munication a | nd Computer Netv | works | |
| Units | INTRODUCTION AND THE PHYSICAL LAYER THE DATA LINK LAYER AND THE MEDIUM ACCESS SUBLAYER THE NETWORK LAYER THE TRANSPORT LAYER THE APPLICATION LAYER AND APPLICATION LAYER PROTOCOLS | | | | |
| Duration | | | 60hours | | |
| Learning Objectives | Understand and apply network applications, hardware, software, and reference models for network communication. Design and analyze data link layer protocols, multiple access protocols, and wireless LAN technologies. Design routing algorithms, congestion control algorithms, and evaluate network layer protocols for internetworking. Analyze transport service, transport protocols, and evaluate UDP and TCP in the internet. Understand and evaluate application layer protocols, including DNS, email WWW and network management protocols | | | | |
| Units | U1 | U2 | U3 | U4 | U5 |
| Hours Split: Total: 60 | 10 | 12 | 14 | 10 | 14 |
| Internal valuation:40marks | 8 | 8 | 8 | 8 | 8 |

| Resource Material: | Study Material(Handouts): https://youtu.be/aPw0pltPrY4 https://youtu.be/OmYHJShD_OM |
|-----------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | Reference Books: 1.Behrouz A. Forouzan (2006), Data communication and Networking, 4th Edition, Mc Graw-Hill, India. 2. Kurose, Ross (2010), Computer Networking: A top down approach, Pearson Education, India. |
| | YouTube Links: https://youtu.be/j0vdP_bi274 https://youtu.be/wdMwg3eHjSM |
| | Power Point Presentations: https://youtu.be/0gqBPLQZYCU https://youtu.be/zUXVDFIOIT8 |
| | |

| UNIT | DESCRIPTION | PEDAGOGY | INTERNAL EVALUATI ON |
|------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------|----------------------------|
| I | Network applications, network hardware, network software, reference models: OSI, TCP/IP, Internet, Connection oriented network - X.25, frame relay. Theoretical basis for communication, guided transmission media, wireless transmission, the public switched telephone networks, mobile telephone system. | P1,P2,P3 | PQ,P6,PT |
| П | Design issues, error detection and correction, elementary data link protocols, sliding window protocols, example data link protocols - HDLC, the data link layer on the internet. Channel allocations problem, multiple access protocols, Ethernet, Data Link Layer switching, Wireless LAN, Broadband Wireless, Bluetooth. | P1,P2,P3,P5 | P6,PT |
| III | Network layer design issues, routing algorithms, Congestion control algorithms, Internetworking, the network layer in the internet (IPv4 and IPv6), Quality of Service. | P1,P2,P3,P5 | PQ,PT |
| IV | Transport service, elements of transport protocol, Simple Transport Protocol, Internet transport layer protocols: UDP and TCP | P1,P2,P4 | PQ,P6,PT |
| V | Domain name system, electronic mail, World Wide Web: architectural overview, dynamic web document and http. APPLICATION LAYER PROTOCOLS: Simple Network Management Protocol, File Transfer Protocol, Simple Mail Transfer Protocol, Telnet. | PQ,P6,PT, P8 | PQ,PT |